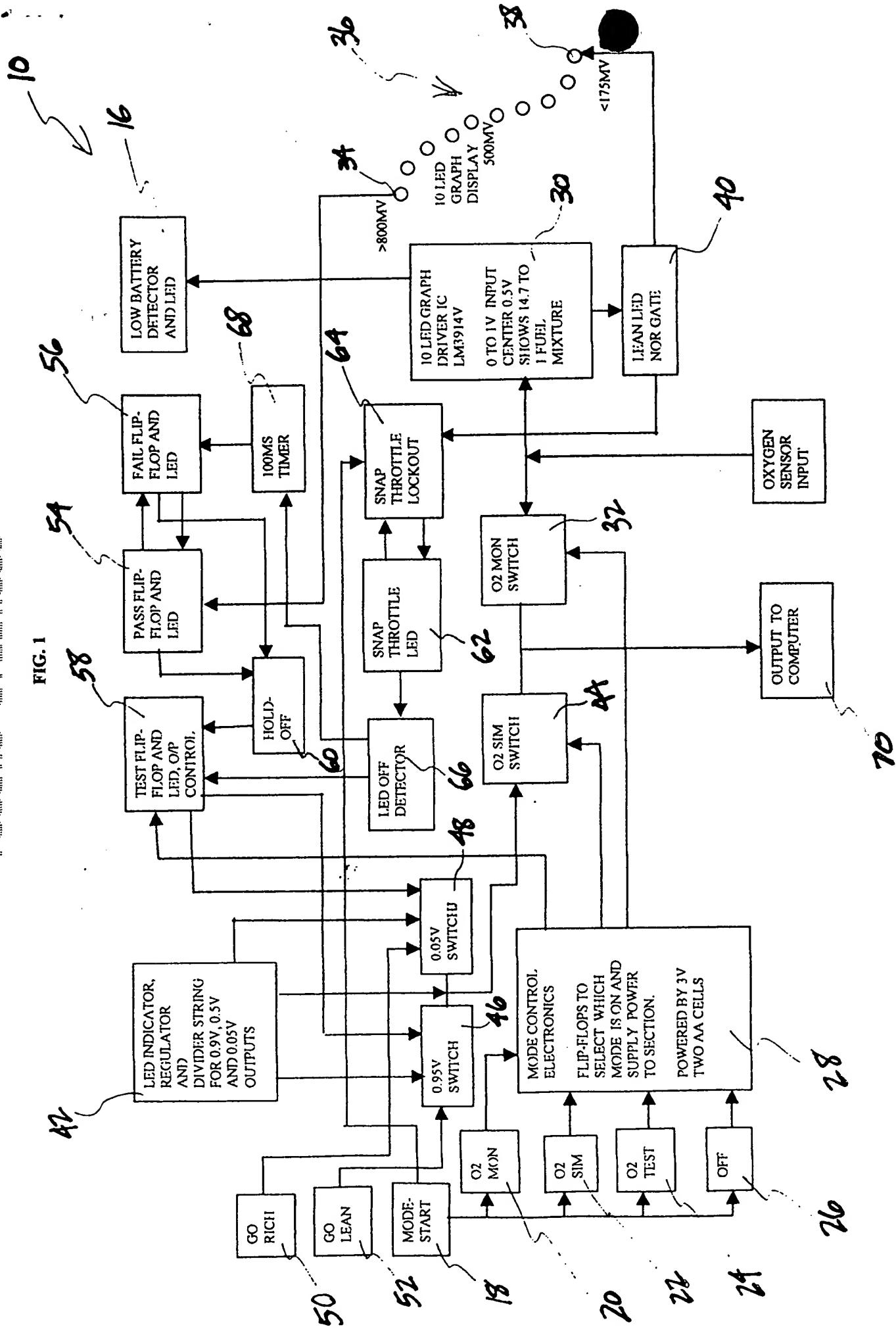


४५८



TEST OUTPUT 950mV
TO COMPUTER

50mV TO COMPUTER

FAIL

800mV

PASS

175mV

800mV

PASS

175mV

START PRESSED THROTTLE LEAN LED SNAPPED OFF 100mS TIMEOUT
TIMER STARTS

FIG. 2

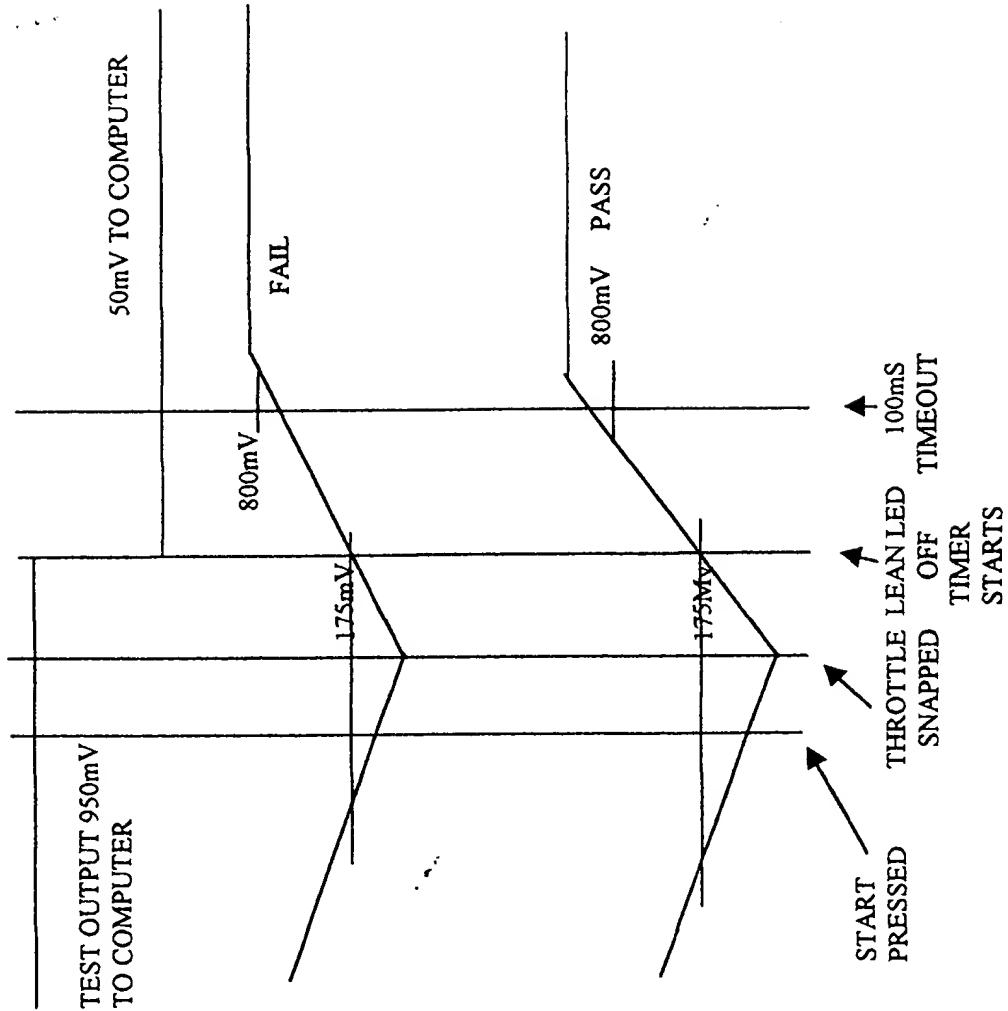
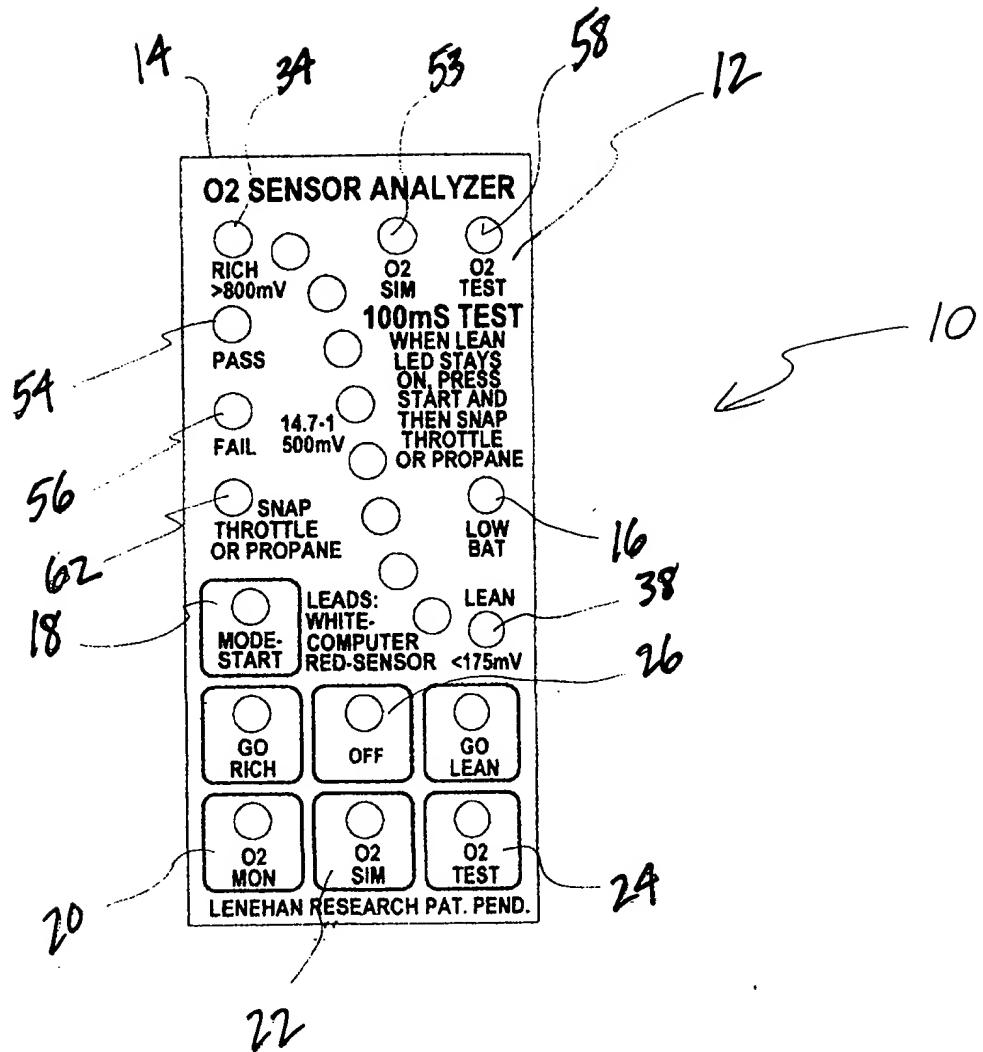


FIG. 3



00000000000000000000000000000000